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FORM PTO-1449

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(use as many sheets as necessary)

Attorney Docket Number	36491/RJP/B600
Application Number	09/493,942
Filing Date	January 28, 2000
Applicant(s)	James Y.C. Chang
Group Art Unit	2811
Examiner Name	To be assigned

U.S. PATENT DOCUMENTS

EXAMINER INITIALS	DOCUMENT NUMBER	ISSUE DATE	PATENTEE	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

FOREIGN PATENT DOCUMENTS

EXAMINER INITIALS	DOCUMENT NUMBER	PUBLICATION DATE	COUNTRY OR PATENT OFFICE	CLASS	SUBCLASS	TRANSLATION	
						YES	NO

OTHER DOCUMENTS

EXAMINER INITIALS	Include name of the author (in CAPITAL LETTERS), title of the article, title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
LD	Burghartz et al., "High-Q Inductors in Standard Silicon Interconnect Technology and its Application to an Integrated RF Power Amplifier," IBM T.J. Watson Research Center, Yorktown Heights, New Jersey, 3 pp. (no date)
LD	Craninckx et al., "A 1.8-GHz Low-Phase-Noise CMOS VCO Using Optimized Hollow Spiral Inductors," <i>IEEE Journal of Solid-State Circuits</i> , Vol. 32, No. 5, May 1997, pp. 736-744.
LD	Long et al., "The Modeling, Characterization, and Design of Monolithic Inductors for Silicon RF IC's," <i>IEEE Journal of Solid-State Circuits</i> , Vol. 32, No. 3, March 1997, pp. 357-369.
LD	Yue et al., "On-Chip Spiral Inductors with Patterned Ground Shields for Si-Based RF IC's," <i>IEEE Journal of Solid-State Circuits</i> , Vol. 33, No. 5, May 1998, pp. 743-751.

EXAMINER SIGNATURE	DONOVAN	DATE CONSIDERED	9-04-01
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U.S. PATENT DOCUMENTS

EXAMINER INITIALS	DOCUMENT NUMBER	ISSUE DATE	PATENTEE	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
LD	5,446,311	8/1995	Ewen et al.	257	531	
LD	5,483,207	1/1996	Gabara	331	117 FE	
LD	5,717,243	2/1998	Lowther	257	531	

FOREIGN PATENT DOCUMENTS

EXAMINER INITIALS	DOCUMENT NUMBER	PUBLICATION DATE	COUNTRY OR PATENT OFFICE	CLASS	SUBCLASS	TRANSLATION	
						YES	NO
LD	EP 0 780 853 A1	6/1997	European Patent Office				
LD	JP 62154607	7/1987	Japan (English abstract only)				
LD	WO 98/50956	11/1998	WIPO				

OTHER DOCUMENTS

EXAMINER INITIALS	Include name of the author (in CAPITAL LETTERS), title of the article, title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.

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Applicant(s)

James Y.C. Chang

Group Art Unit

2811

Examiner Name

To be assigned

U.S. PATENT DOCUMENTS

EXAMINER INITIALS	DOCUMENT NUMBER	ISSUE DATE	PATENTEE	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
LD	4,789,976	12/1988	Fujishima	369	54	
LD	4,818,903	4/1989	Kawano	307	521	
LD	5,031,233	7/1991	Ragan	455	307	
LD	5,828,589	10/1998	Degenhardt	364	724.19	

FOREIGN PATENT DOCUMENTS

EXAMINER INITIALS	DOCUMENT NUMBER	PUBLICATION DATE	COUNTRY OR PATENT OFFICE	CLASS	SUBCLASS	TRANSLATION	
						YES	NO
LD	DE 19506324 C1	10/1995	Germany (English family patent, U.S. 5,828,589 above)				
LD	DE 3723778 A1	1/1988	Germany (English family patent, U.S. 4,818,903 above)				
LD	EP 0393717 A2	10/1990	European Patent Office				

OTHER DOCUMENTS

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LD	Poole et al., "A CMOS Subscriber Line Audio Processing Circuit Including Adaptive Balance," IEEE Proceedings of the International Symposium on Circuits and Systems, US, New York, Vol. Conf. 21, 1988, pp. 1931-1934. (no month)

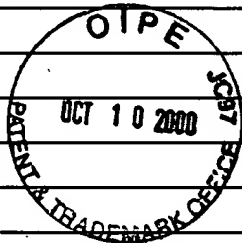
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U.S. PATENT DOCUMENTS						
EXAMINER INITIALS	DOCUMENT NUMBER	ISSUE DATE	PATENTEE	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
LD	5,329,143	7/1994	Chan et al.	257	173	
LD	5,739,587	4/1998	Sato	257	758	
LD	5,825,600	10/1998	Watt	361	56	



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EXAMINER INITIALS	DOCUMENT NUMBER	PUBLICATION DATE	COUNTRY OR PATENT OFFICE	CLASS	SUBCLASS	TRANSLATION	
						YES	NO
LD	EP 0535536 A1	4/1993	European Patent Office				

OTHER DOCUMENTS	
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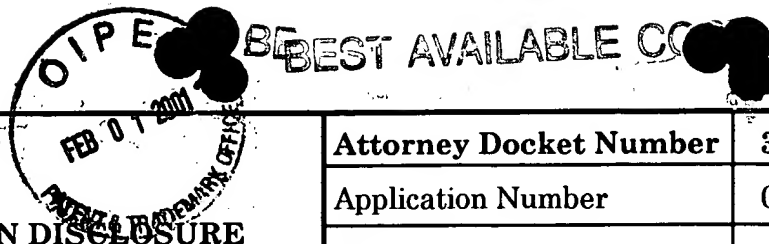
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Applicant(s)	James Y.C. Chang
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FOREIGN PATENT DOCUMENTS

EXAMINER INITIALS	DOCUMENT NUMBER	PUBLICATION DATE	COUNTRY OR PATENT OFFICE	CLASS	SUBCLASS	TRANSLATION	
						YES	NO
LD	GB 2120478 A	11/1983	United Kingdom				

OTHER DOCUMENTS

EXAMINER INITIALS	Include name of the author (in CAPITAL LETTERS), title of the article, title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
LD	Nguyen, N. and Meyer, R., "Si IC-Compatible Inductors and LC Passive Filters," IEEE Journal of Solid-State Circuits, US, New York, No. 4, August 25, 1990, pp. 1028-1031

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EXAMINER INITIALS	DOCUMENT NUMBER	ISSUE DATE	PATENTEE	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
LD	4,789,976	12/1988	Fujishima	369	54	
LD	5,715,012	2/1998	Patel et al.	348	555	
LD	5,737,035	4/1998	Rotzoll	348	725	

FOREIGN PATENT DOCUMENTS

EXAMINER INITIALS	DOCUMENT NUMBER	PUBLICATION DATE	COUNTRY OR PATENT OFFICE	CLASS	SUBCLASS	TRANSLATION	
						YES	NO
LD	EP 0 629 040 A1	12/1994	EPO				

OTHER DOCUMENTS

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LD	Poole, S., Surace, G., Singh, B., Dyer, N., "A CMOS Subscriber Line Audio Processing Circuit Including Adaptive Balance," IEEE International Symposium on Circuits and Systems, Finland, Espoo, Volume 2 of 3, June 7-9, 1988, pp. 1931-1934 (no month)

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